

Applicant: Dan M. White
Serial No.: 10/748,427
Filed : December 30, 2003
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Attorney's Docket No.: Intel-017PUS
Intel Docket Number: P17944

AMENDMENTS TO THE DRAWINGS:

The attached replacement 8 sheets of drawings include changes to FIGS. 1 to 8 and replace the 8 original sheets including FIGS. 1 to 8.

In Figures 1 to 7, handwritten text and other informalities were replaced with typed text.

In Figure 8, duplicate reference numbers were replaced with new reference numbers and handwritten text and other informalities were replaced with typed text.

Attachments following last page of this Amendment:

Replacement Sheet (8 pages)

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REMARKS

Claims 1 to 3, 5, 7, 9 to 13, 15 to 18, 20 and 24 are pending in this application of which claims 1, 13 and 16 are the independent claims. Claims 4, 6, 8, 14, 19 and 21 to 23 are cancelled without prejudice. Favorable reconsideration and further examination are respectfully requested.

Claims 1 to 5, 7, 12 to 15 and 16 to 20 were rejected under 35 U.S.C. § 102(b) as being obvious over Bade et al. (U.S. Patent Publication Number 20020059054 hereinafter "Bade").

Claims 6, 8 and 21 were rejected under 35 U.S.C. § 103(a) as being obvious over Bade in view of Hall et al. (U.S. Patent Number 4,720,778 hereinafter "Hall"). Claims 9 and 22 were rejected under 35 U.S.C. § 103(a) as being obvious over Bade in view of Hoff et al. (U.S. Patent Number 5,778,231 hereinafter "Hoff"). Claims 10 and 23 was rejected under 35 U.S.C. § 103(a) as being obvious over Bade in view of Hoff and in view of Hall. Claims 11 and 24 were rejected under 35 U.S.C. § 103(a) as being obvious over Bade in view of Smith et al. (U.S. Patent Number 6,311,324 hereinafter "Hoff").

Amended claim 1 is directed to a method of displaying embedded firmware program information. The method includes displaying a first screen to interact with a user for high level function selections, displaying a second screen to show hardware resources for a programmable circuit, displaying a third screen to show source code for a plurality of source code programs to control the programmable circuit and displaying a fourth screen to render symbolic information associated with the displayed source code. The symbolic information includes code labels, data labels referring to data structures, data register names, and index register names; address

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locations for the code labels and the data labels; and listings associated with named registers, data labels for word, byte and short entities, and names of the data structures individually expandable to show addresses and values.

The applied art is not understood to disclose or to suggest the foregoing features of claim 1. In particular, Bade does not disclose or suggest that the symbolic information includes code labels, data labels referring to data structures, data register names, and index register names; address locations for the code labels and the data labels; and listings associated with named registers, data labels for word, byte and short entities, and names of the data structures individually expandable to show addresses and values.

As indicated by the Examiner Bade does not disclose or suggest symbolic information associated with data structures and displaying address and value information (see page 9 of the Office Action).

The Examiner has indicated that Hall displays a symbolic information associated with data structures and displaying address and value information associated with data. However, Hall does not disclose or suggest symbolic information as recited in amended claim 1. For example, Hall does not disclose or suggest one screen to show symbolic information including code labels, data labels referring to data structures, data register names, index register names, address locations for the code labels and the data labels and listings including named registers, data labels for word, byte and short entities, and names of the data structures individually expandable to show addresses and values.

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Accordingly, even if Hall was combined with Bade, the hypothetical combination would not disclose or suggest that the symbolic information includes code labels, data labels referring to data structures, data register names, and index register names; address locations for the code labels and the data labels; and listings associated with named registers, data labels for word, byte and short entities, and names of the data structures individually expandable to show addresses and values.

Applicants submit that the Examiner has failed to show proper motivation for one of ordinary skill in the art to combine the cited art to display in one screen the symbolic information as recited in amended claim 1.

Claims 13 and 18 include the corresponding feature of symbolic information as recited in claim 1. Applicants submit that the cited art should also be withdrawn with respect to claims 13 and 18 for at least the same reasons as claim 1.

Also, claim 18 is further distinguished from the cited art because the cited art does not disclose or suggest parsing the source code to create the listings in the fourth screen and outputting symbolic information for a data structure recursively until resultant fields are no longer structures.

Applicant submits that all dependent claims now depend on allowable independent claims.

For at least the foregoing reasons, Applicant requests withdrawal of the art rejections.

It is believed that all of the pending claims have been addressed. However, the absence of a reply to a specific rejection, issue or comment does not signify agreement with or

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concession of that rejection, issue or comment. In addition, because the arguments made above may not be exhaustive, there may be reasons for withdrawing the prior art cited with regards to any or all pending claims (or other claims) that have not been expressed. Finally, nothing in this paper should be construed as intent to concede any issue with regard to any claim, except as specifically stated in this paper, and the amendment of any claim does not necessarily signify concession of unpatentability of the claim prior to its amendment.

Applicant submits that the entire application is now in condition for allowance. Such action is respectfully requested at the Examiner's earliest convenience.

Applicant's attorney can be reached by telephone at (781) 401-9988 ext. 23.

No fee is believed to be due for this Response; however, if any fees are due, please apply such fees to Deposit Account No. 50-0845 referencing Attorney Docket: Intel-017PUS.

Respectfully submitted,



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